

REMARKS

By this response the pending claims have been revised so that they include a limitation which Applicants believe to be undisclosed in the Funakoshi publication which is the rejecting reference.

In particular, Claim 1, which is the only pending independent claim, has been amended to specify that the slice check wire is disposed on a first wire layer on the substrate. While applicants acknowledge that the cited Funakoshi reference discloses a slice check wire, that reference is believed to be silent as to the particular layer on which the slice check wire is disposed. The importance of providing the slice check wire on the first layer is stressed in Applicants' Specification in the paragraph bridging Pages 15 and 16, as well as in the succeeding paragraph, wherein it states:

“As a result, it is now possible to by far reliably detect defective devices if compared with conventional visual examination processes. Additionally, it is also possible to examine each device by means of the slice check wire at significant points in the subsequent steps so that absolutely no defective devices may be detected after bonding a plurality of optical sensor panels for each device.

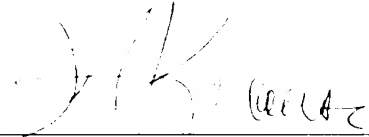
Particularly, since pixel destructions due to static electricity can occur anytime until the TFT driver and the photoelectric conversion element driver are mounted and become electrically operable, the examination using the slice check wire may have to be repeated until that time.”

For this reason, Applicants believe that independent Claim 1, and all of the pending dependent claims are in condition for allowance.

request issuance of a formal Notice of Allowance.

Applicants' undersigned attorney may be reached in our New York office by telephone at (212) 218-2100. All correspondence should continue to be directed to our below listed address.

Respectfully submitted,



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